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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/574,315	03/30/2006	Mickael Guibert	034299-688	7117
46188 7590 04/30/2007 THELEN REID BROWN RAYSMAN & STEINER LLP P. O. BOX 640640			EXAMINER	
			TAN, VIBOL	
SAN JOSE, CA 95164-0640		ART UNIT	PAPER NUMBER	
•	,		2819	
			MAIL DATE	DELIVERY MODE
	*	•	04/30/2007	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

		Application No.	Applicant(s)				
Office Action Summary		10/574,315 GUIBERT ET AL.		••			
		Examiner	Art Unit				
		Vibol Tan	2819	1			
Period fo	The MAILING DATE of this communication a		sheet with the correspondence a	ddress			
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WHIC - Exter after - If NO - Failui Any r	HEVER IS LONGER, FROM THE MAILING usions of time may be available under the provisions of 37 CFR SIX (6) MONTHS from the mailing date of this communication. period for reply is specified above, the maximum statutory perior to reply within the set or extended period for reply will, by state the provision of the main department of the main department. See 37 CFR 1.704(b).	DATE OF THIS COM 1.136(a). In no event, however, but will apply and will expire SI tute, cause the application to be	MMUNICATION. er, may a reply be timely filed X (6) MONTHS from the mailing date of this become ABANDONED (35 U.S.C. § 133).				
Status							
1)[\]	Responsive to communication(s) filed on <u>03</u>	March 2006		•			
·		nis action is non-final					
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-,_	closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.						
Dispositi	on of Claims	•					
_			•				
•	I)⊠ Claim(s) <u>1-18</u> is/are pending in the application. 4a) Of the above claim(s) is/are withdrawn from consideration.						
	Claim(s) is/are allowed.	iawii iioiii considerat					
6)⊠ Claim(s) <u>1-3</u> is/are allowed.							
· -	Claim(s) <u>4-18</u> is/are objected to.						
· · · · · · · · · · · · · · · · · · ·	Claim(s) are subject to restriction and	l/or election requirem	ent.				
Annlicati	on Papers						
•	The specification is objected to by the Exami		ated to by the Everyines	•			
•	The drawing(s) filed on is/are: a) ☐ a Applicant may not request that any objection to the	, , , , ,	*				
	Replacement drawing sheet(s) including the corre			SER 1 121(d)			
11)□	The oath or declaration is objected to by the	•		, ,			
	nder 35 U.S.C. § 119						
		an mainaith condon 25 l					
_	Acknowledgment is made of a claim for forei ☑ All b)□ Some * c)□ None of:	gn priority under 35 t	J.S.C. 9 119(a)-(d) or (f).				
a)L	1. ☐ Certified copies of the priority docume	ents have been receiv	ved.				
	2. Certified copies of the priority docume						
	3. Copies of the certified copies of the pr			il Stage			
	application from the International Bure			14.3			
* S	ee the attached detailed Office action for a li	,					
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Attachment	·	_					
	e of References Cited (PTO-892) e of Draftsperson's Patent Drawing Review (PTO-948)		terview Summary (PTO-413) aper No(s)/Mail Date				
3) 🔀 Inform	nation Disclosure Statement(s) (PTO/SB/08)	5) 🔲 N	otice of Informal Patent Application				
Paper	No(s)/Mail Date <u>7/27/06</u> .	6) <u> </u>	ther:	,			

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DETAILED ACTION

Claim Objections

1. Claim 3 is objected to because of the following informalities: claim 3, line 2; delete [les] before configurations. Appropriate correction is required.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 3. Claims 1-3 are rejected under 35 U.S.C. 102(b) as being anticipated by Leeds et al. (U.S. Pat. 5,760,607).

In claim 1, Leeds et al. teaches all claimed features in Figs. 7 and 8, component with a dynamically reconfigurable architecture for processing data comprising a data processing block TD (comprising 37a, 37b) and a general controller CG (30) capable of controlling (Fig. 7) the data processing block TD characterized in that: the block TD comprises a plurality of reconfigurable elementary data processing blocks BE (37a, 37b); each elementary block BE comprises two inputs, E1 and E2 (inputs coupling to 34, 36) for reception of data to be processed, and one output S (76) for transmission of processed data; a common input data bus (34 in Fig. 10) being capable of transmitting data to be processed to the input E1 of each of the blocks BE (37a, 37b) and the controller CG (30); for each block BE, an output data bus (76) connected to its output S, being capable of transmitting processed data outside the component and through a

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bypass data bus to the input E2 (second input coupling to 76b of 37c) of a single other block BE (37c); the controller CG (30) is capable of initializing configurations (col. 6, line 25; at the start of configuration...) of blocks BE and controlling their dynamic reconfiguration (30 providing clock signal 35), controlling data flows (38) at the output from each block BE so as to transmit data either towards the outside or to the input E2 of another block BE, and controlling data flows (various types in Fig. 7a-7e) at the input of each block BE.

In claim 2, Leeds et al. further teaches the component according to claim 1 in which the controller CG (30) is capable of controlling transmission of data received from the outside on the common input data bus (76) as and when they arrive, in sequence to each of the blocks BE, the data being transmitted to the next block BE (from 37a to 37b) when the maximum processing capacity of the previous block BE is reached (col. 6, line 45; configuration is complete).

In claim 3, Leeds et al. further teaches the component according to claim 1 in which the controller CG (30) is capable of initializing configurations (col. 6, line 26; at the start of configuration, memory circuit 30 provides preamble data to 37) of blocks BE block by block, and controlling the dynamic (50, 51) reconfiguration of blocks, block by block, so as to make the block TD capable of processing data in pipeline mode (Fig. 8 shows pipeline mode) or in parallel mode or in combined mode.

4. Claims 4-18 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to Vibol Tan whose telephone number is (571) 272-1811. The examiner can normally be reached on Monday-Friday (7:00 AM-4:30 PM).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Rexford Barnie can be reached on (571) 272-7492. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

VIBOLTAN PRIMARY EXAMINER